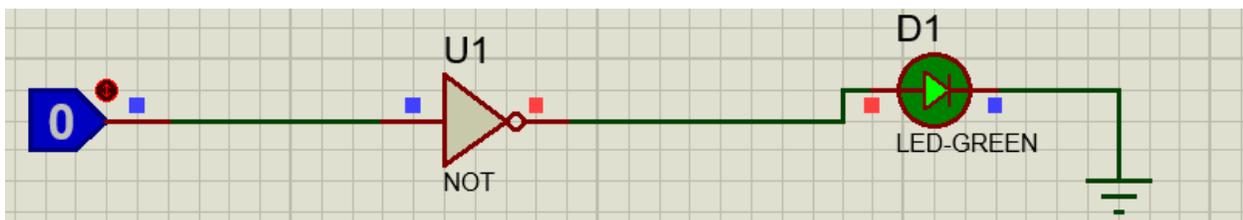
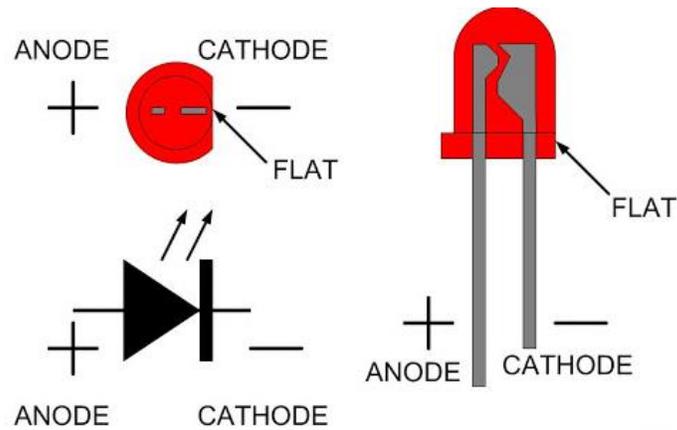
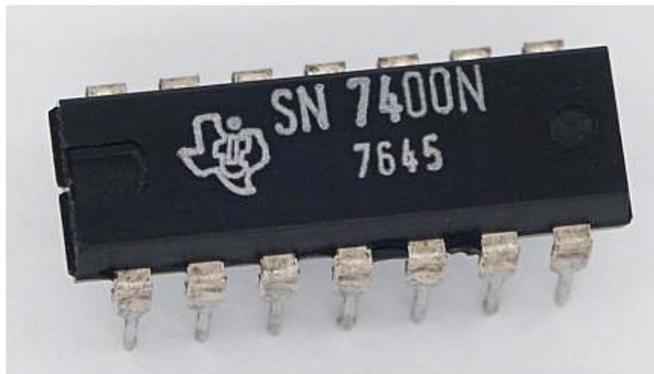
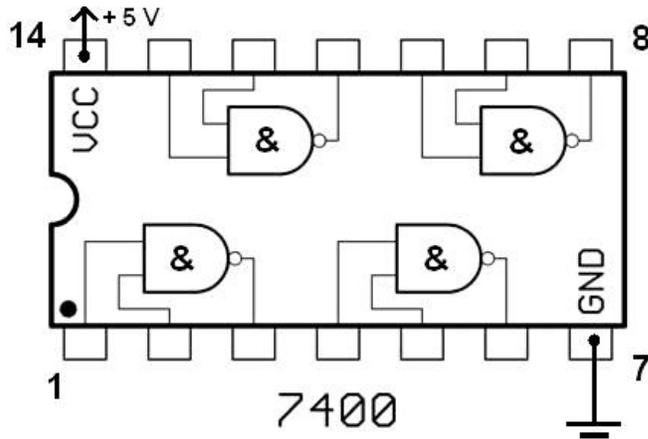


Led and NAND Lab in proteus

connect led in proteus



NAND IC



PINS		Data
A1	Pin 1	Pin 1 will be the input of the first NAND gate in IC 74LS00.
B1	Pin 2	Pin 2 will be used as the second Input of First NAND gate.
Y1	Pin 3	The output of the first NAND gate will be available at Pin 3.
A2	Pin 4	Pin 4 will be used for the input of the second NAND gate.
B2	Pin 5	The second input of the second NAND gate will be given at Pin 5.
Y2	Pin 6	The output of the second NAND gate will be available at Pin 6.
GND	Pin 7	Pin 7 will be used at the common ground by devices and the power supply that will use with 74LS00.
Y3	Pin 8	Third NAND gate output will be at Pin 8.
A3	Pin 9	Pin 9 will be used as the first input pin for the third NAND gate.
B3	Pin 10	Pin 10 will be the second input of the third NAND gate.
Y4	Pin 11	The output of the fourth NAND gate will available at Pin 11.
A4	Pin 12	Pin 12 will be used for the first input of the 4 th NAND gate.
B4	Pin 13	The second input of the fourth NAND gate will be at Pin 13.
Vcc	Pin 14	Pin 14 will be used to give power to the IC.

Components

- Led
- Resistor (330 ohm)
- Jumpers
- 74ls00(NAND ic)
- Breadboard

