

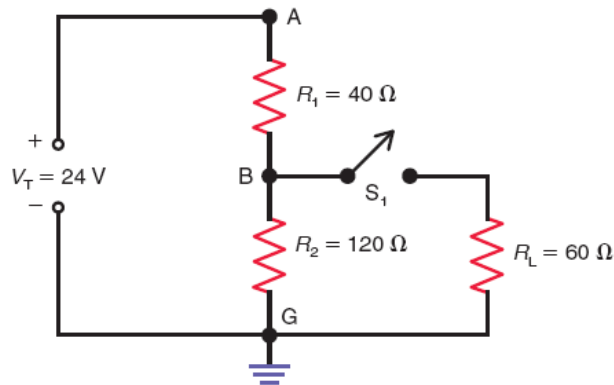
Answer all questions.

No. of questions: 5

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**Solution to Question 1**

a) Calculate  $I_1$ ,  $I_2$ ,  $I_L$ ,  $V_{BG}$ ,  $V_{AG}$  when (i)  $S_1$  is open, (ii)  $S_2$  is closed.



**Answer**

(i) When  $S_1$  open,

$$I_L = 0$$

$R_1$  and  $R_2$  are in series,  $R_T = R_1 + R_2 = 40 + 120 = 160 \Omega$

$$I_1 = I_2 = 24/160 = 0.15 \text{ A}$$

$$V_{BG} = I_2 R_2 = 0.15 * 120 = 18 \text{ V}$$

$$V_{AG} = V_T = 24 \text{ V}$$

(ii) When  $S_2$  is closed,

$$R_{L2} = R_L // R_2 = (60)(120)/(60+120) = 40 \Omega$$

$$R_T = R_1 + R_{L2} = 40 + 40 = 80 \Omega$$

$$I_T = I_1 = 24/80 = 0.3 \text{ A}$$

By current divider rule,

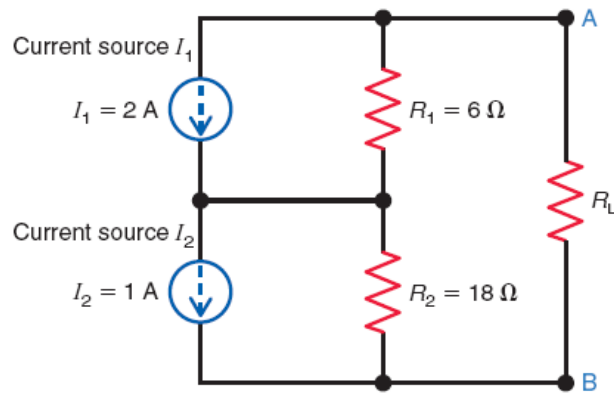
$$I_2 = (0.3)*(60)/(60+120) = 0.1 \text{ A}$$

$$I_L = (0.3)*(120)/(60+120) = 0.2 \text{ A}$$

$$V_{BG} = I_2 R_2 = 0.1 * 120 = 12 \text{ V}$$

$$V_{AG} = V_T = 24 \text{ V}$$

**b)** Find the value of  $R_L$  for maximum power transfer in circuit shown. What is this maximum power?



**Answer**

Using the conversion from Norton equivalent circuit to thevenin equivalent circuit,

For current source  $I_1$  and resistance  $R_1$  convert to,

$$V_{th1} = I_1 R_1 = 2 * 6 = 12 \text{ V} \quad R_{th1} = R_1 = 6 \Omega$$

$$V_{th2} = I_2 R_2 = 1 * 18 = 18 \text{ V} \quad R_{th2} = R_2 = 18 \Omega$$

The total equivalent circuit consists of a battery of voltage,

$$V = V_{th1} + V_{th2} = 12 + 18 = 30 \text{ V}$$

in series with a resistance  $R = R_{th1} + R_{th2} = 6 + 18 = 24 \Omega$

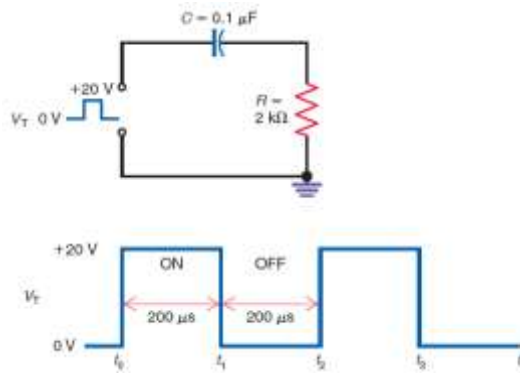
in series with  $R_L$ .

For maximum power transfer,  $R_L = R = 24 \Omega$

$$\text{The maximum power } P_{max} = \frac{V^2}{4R} = \frac{(30)^2}{(4)(24)} = 9.375 \text{ W}$$

**Solution to Question 2**

**a)** Find the capacitor voltage  $V_C(t)$  and the resistor voltage  $V_R(t)$  for the circuit shown in figure. Plot these quantities.



## Answer

The time constant  $\tau = RC = (2k) \cdot (0.1\mu) = 0.2 \text{ ms} = 200 \mu\text{s}$

For  $0 \leq t \leq 200 \mu\text{s}$ , the capacitor is charging.

$$V_C(0) = 0$$

$$V_C(200\mu\text{s}) = 20(1 - e^{-200/200}) = 12.64 \text{ V}$$

$$V_R(0) = 20 \text{ V}$$

$$V_R(200\mu\text{s}) = 20 e^{-200/200} = 7.36 \text{ V}$$

For  $200 \mu\text{s} \leq t \leq 400 \mu\text{s}$ , the capacitor is discharging.

$$V_C(200\mu\text{s}) = 12.6 \text{ V}$$

$$V_C(400\mu\text{s}) = 12.6 e^{-200/200} = 4.64 \text{ V}$$

$$V_R(200\mu\text{s}) = -12.6 \text{ V}$$

$$V_R(400\mu\text{s}) = -12.6 e^{-200/200} = -4.64 \text{ V}$$

For  $400 \mu\text{s} \leq t \leq 600 \mu\text{s}$ , the capacitor is charging.

$$V_C(400\mu\text{s}) = 4.64 \text{ V}$$

$$V_C(600\mu\text{s}) = 4.64 + (20 - 4.64)(1 - e^{-200/200}) = 14.35 \text{ V}$$

$$V_R(400\mu\text{s}) = 20 - 4.64 = 15.36$$

$$V_R(600\mu\text{s}) = 15.36 e^{-200/200} = 5.65 \text{ V}$$

For  $600 \mu\text{s} \leq t \leq 800 \mu\text{s}$ , the capacitor is discharging.

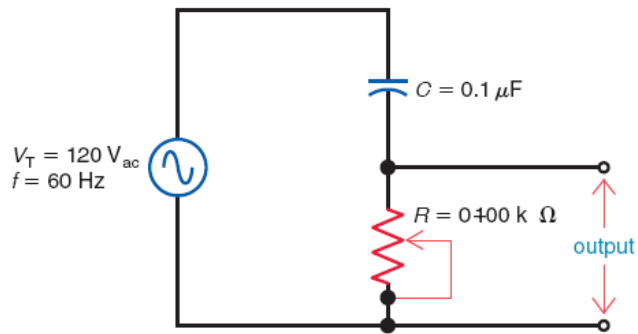
$$V_C(600\mu\text{s}) = 14.35 \text{ V}$$

$$V_C(800\mu\text{s}) = 14.35 e^{-200/200} = 5.28 \text{ V}$$

$$V_R(600\mu s) = -14.35 \text{ V}$$

$$V_R(800\mu s) = -14.35e^{-200/200} = -5.28 \text{ V}$$

**b)** For the phase shifter shown, what is the phase shift between  $V_T$  and  $V_R$  and between  $V_T$  and  $V_C$ . Draw the voltage phasor diagram with  $V_T$  as reference voltage.



**Answer**

$$X_C = 1/2\pi fC = 1/2\pi(60)(0.1 \times 10^{-6}) = 26.53 \text{ k}\Omega$$

The phase angle between  $V_T$  and  $V_R$  is  $\phi$ ,

$$\tan \phi = X_C/R = -26.53\text{k}/400\text{k} = -0.06633$$

$$\phi = -3.8^\circ$$

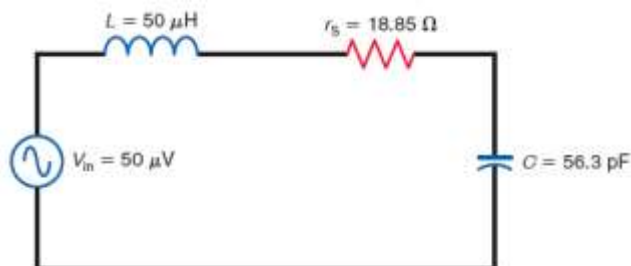
The phase angle between  $V_T$  and  $V_C$  is  $\theta$ ,

$$\tan \theta = -R/X_C = -400\text{k}/26.53\text{k} = -15.08$$

$$\theta = -86.21^\circ$$

**Solution to Question 3**

**a)** For the RLC circuit at resonance, find  $f_r$ ,  $Z$ ,  $I$ ,  $V_C$  and  $V_L$  at  $f_r$ ,  $V_R$ , quality factor and bandwidth.



**Answer**

The RLC circuit at resonance,

$$f_r = 1/2\pi\sqrt{LC} = 1/2\pi\sqrt{(50 \times 10^{-6})(56.3 \times 10^{-12})}$$

$$= 3 \times 10^6 \text{ Hz} = 3 \text{ MHz}$$

$$Z = R = 18.85 \ \Omega$$

$$I = V/Z = V/R = 50 \mu / 18.85 = 2.65 \ \mu\text{A}$$

$$V_C = IX_C = I/2\pi f_r C = 2.65 \times 10^{-6} / 2\pi(3 \times 10^6)(56.3 \times 10^{-12})$$

$$= 2.5 \times 10^{-3} \text{ V} = 2.5 \text{ mV}$$

$$V_L = IX_L = I(2\pi f_r L) = (2.65 \times 10^{-6})(2\pi \times 3 \times 10^6 \times 50 \times 10^{-6})$$

$$= 2.5 \times 10^{-3} \text{ V} = 2.5 \text{ mV}$$

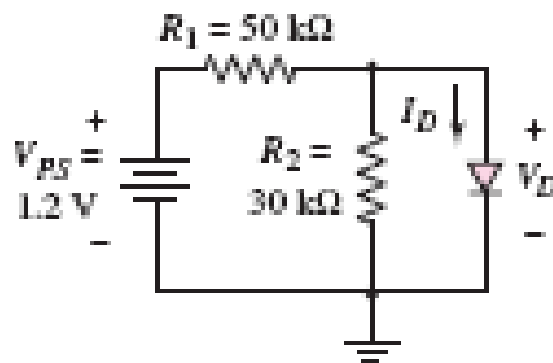
$$V_R = V_{in} = 50 \ \mu\text{V}$$

$$Q = X_L/R = 2\pi f_r L/R = 2\pi(3 \times 10^6)(50 \times 10^{-6})/18.85$$

$$= 50$$

$$\text{Bandwidth} = f_r/Q = 3 \times 10^6 / 50 = 60 \times 10^3 \text{ Hz} = 60 \text{ kHz}$$

**b)** The diode in the circuit shown in Figure has a reverse-saturation current. Determine the diode voltage and current if (i) the diode is ideal (ii) real diode with internal resistance of 500  $\Omega$ .



**Answer**

By Thevenin theorem,

$$R_{th} = R_1 // R_2 = 50 \times 30 / (50 + 30) = 18.75 \text{ k}\Omega$$

$$V_{th} = 12 \times 30 / (50 + 30) = 4.5 \text{ V}$$

(i) The diode is ideal. It is represented as closed switch.

$$V_D = 0$$

$$I_D = 4.5/18.75 = 0.24 \text{ A}$$

(ii) Real diode with internal resistance of  $500 \Omega$ ,

$$\text{Applying KVL: } -4.5 + 18.75k I_D + 0.7 + 500 I_D = 0$$

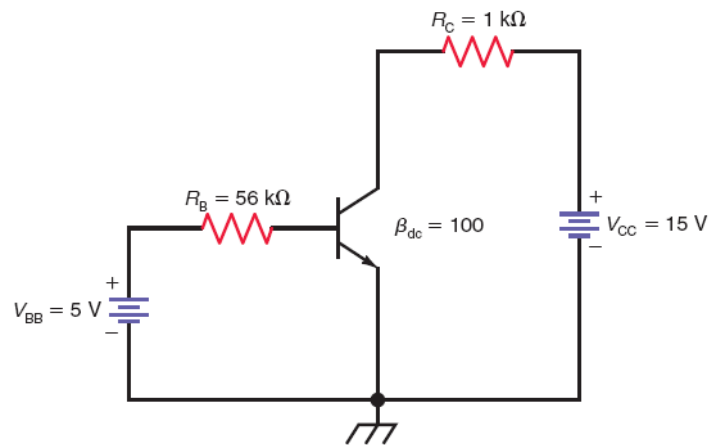
$$I_D = (4.5 - 0.7)/(18.75k+500) = 0.197 \text{ mA}$$

$$V_D = 0.7 + (500)(0.197\text{m}) = 0.7985 \text{ V}$$

### **Solution to Question 4**

**a)** Find  $I_B$ ,  $I_C$ ,  $I_E$  and  $V_{CE}$  in the transistor circuit shown in figure.

$$V_{BEon} = 0.7 \text{ V}$$



### **Answer**

Applying KVL to the base circuit:

$$-5 + 56k I_B + 0.7 = 0$$

$$I_B = (5 - 0.7)/56k = 0.0768 \text{ mA}$$

$$I_C = \beta I_B = 100 * 0.0768\text{m} = 7.68 \text{ mA}$$

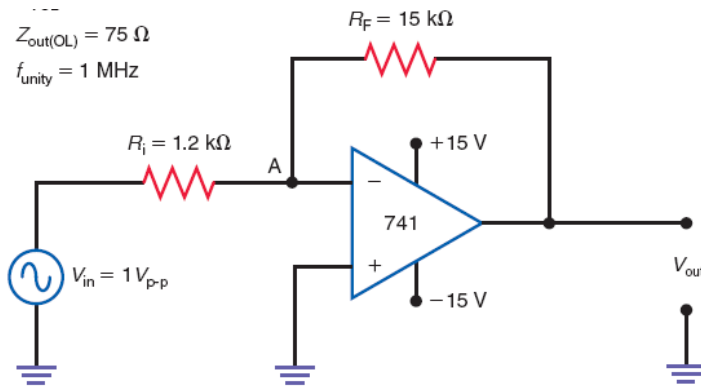
$$I_E = I_C + I_B = 7.68\text{m} + 0.0768\text{m} = 7.7568 \text{ mA}$$

Applying KVL to the collector circuit:

$$-15 + 1k I_C + V_{CE} = 0$$

$$V_{CE} = 15 - (1k)(7.68\text{m}) = 7.32 \text{ V}$$

**b)** Find the output voltage  $V_{out}$  for each of the following operational amplifier circuits.

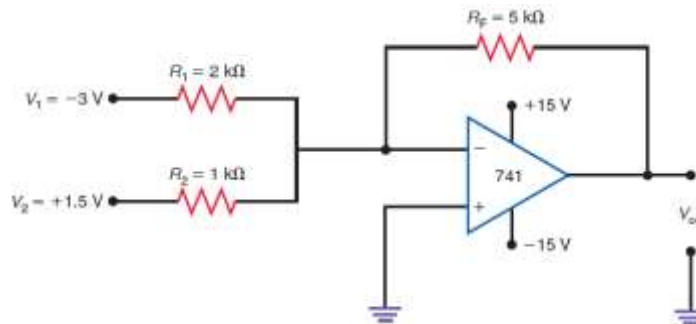


The circuit is inverting operational amplifier.

The gain,

$$A = R_F/R_i = 15k/1.2k = 12.5$$

$$V_{out} = A V_{in} = (12.5)*(1V_{p-p}) = 12.5 V_{p-p}$$



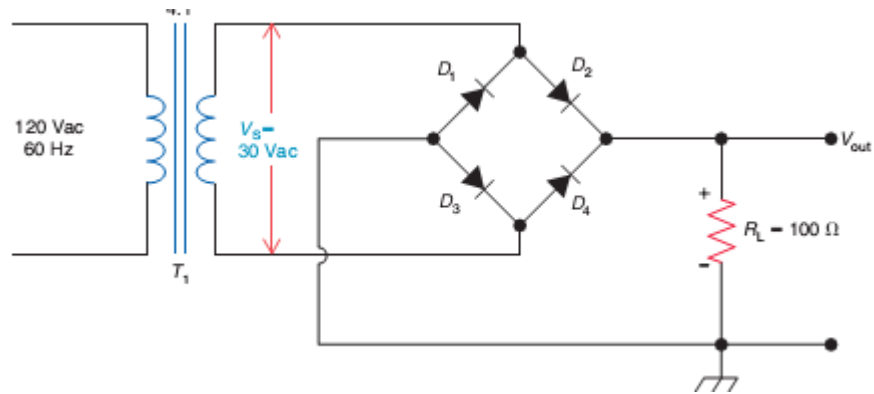
The circuit is a summing operational amplifier,

$$\begin{aligned} V_{out} &= R_F/R_1 V_1 + R_F/R_2 V_2 \\ &= (5/2)(-3) + (5/1)(1.5) \\ &= -4.167 V \end{aligned}$$

### **Solution to Question 5**

**a)** Draw the half-wave rectifier circuit  $V_S$ ,  $V_{outp}$ ,  $V_{dc}$ ,  $I_L$ ,  $I_{diode}$ , PIV to rectify AC voltage of 220 V, 50 Hz. The transformer ratio is 8:1 and  $R_L = 100 \Omega$ . Use second approximation for diode. If a 1000  $\mu F$  capacitor is added to the output, calculate  $V_{ripple}$ ,  $V_{dc}$ ,  $I_L$ , PIV.

### **Answer**



$$V_s = V_p (N_s/N_p) = 220 (1/8) = 27.5 \text{ V}$$

$$V_{\text{speak}} = 27.5 * \sqrt{2} = 38.885 \text{ V}$$

$$V_{\text{outpeak}} = 38.885 - 1.4 = 37.485 \text{ V}$$

$$V_{\text{dc}} = 37.485 * 0.636 = 23.84 \text{ V}$$

$$I_L = V_{\text{dc}}/R_L = 23.84/100 = 0.2384 \text{ A} = 238.4 \text{ mA}$$

$$I_{\text{diode}} = I_L/2 = 238.4/2 = 119.2 \text{ mA}$$

$$\text{PIV} = 38.885 - 0.7 = 38.185 \text{ V}$$

$$\text{Time constant} = R_L C = (100) * (1000 \mu) = 0.1 \text{ s}$$

$$\text{Discharge time} = 1/50 = 0.02 \text{ s}$$

$$V_{\text{ripple}} = V_{\text{outpeak}} (1 - e^{-t/RLC})$$

$$= 37.485 (1 - e^{-0.02/0.1}) = 0.2526 \text{ V}_{\text{p-p}}$$

$$V_{\text{dc}} = V_{\text{outpeak}} - V_{\text{ripple}}/2 = 37.485 - 0.1263 = 37.3587 \text{ V}$$

**b)** Choose the correct answer, **Justify** your choice.

1. A  $2.2\text{k}\Omega$   $R_1$  is in parallel with a  $3.3\text{k}\Omega$   $R_2$ . If these two resistors carry a total current of  $7.5 \text{ mA}$ , how much is the applied voltage?

- (a)  $16.5 \text{ V}$       (b)  $24.75 \text{ V}$       **(c)  $9.9 \text{ V}$**       (d)  $41.25 \text{ V}$ .

**Answer**

$$R = 2.2 * 3.3 / (2.2 + 3.3) = 1.32 \text{ k}\Omega$$

$$V = IR = (7.5\text{m}) * (1.32\text{k}) = 9.9 \text{ V}$$

2. A sine wave whose rms voltage is  $25.2 \text{ V}$  has a peak value of .....

- (a)  $17.8 \text{ V}$       (b)  $16 \text{ V}$       (c)  $50.4 \text{ V}$       **(d)  $35.6 \text{ V}$**

**Answer**



$$V_{\text{peak}} = 25.2\sqrt{2} = 36.6 \text{ V}$$

3. An RC low-pass filter uses a 2.2 k $\Omega$  resistor and a 0.01  $\mu$ F capacitor. What is its cutoff frequency?

- (a) 3.5 MHz.      (b) 72.3 Hz.      **(c) 7.23 kHz.**      (d) 1.59 kHz.

**Answer**

$$\begin{aligned} f_{\text{cutoff}} &= 1/2\pi RC = 1/2\pi(2.2\text{k})(0.01\mu) \\ &= 7.23 \times 10^3 \text{ Hz} = 7.23 \text{ kHz} \end{aligned}$$

4. In a p-type semiconductor, the majority current carriers are .....  
(a) free electrons.      (b) valence electrons.      (c) protons.      **(d) holes.**

**Answer**

The p-type semiconductor is doped with trivalent impurity atoms as Boron. The valence electrons of Boron are bonded to silicon atoms leaving one missing bond that serves as a hole. So the number of holes are increased without increasing the number of electrons.

5. What is the dc output voltage of an unfiltered half-wave rectifier whose peak output voltage is 9.8 V ?

- (a) 6.23 V.**      (b) 19.6 V.      (c) 9.8 V.      (d) 3.1 V.

**Answer**

$$V_{\text{dc}} = 9.8 * 0.636 = 6.23 \text{ V}$$

6. A bipolar junction transistor has .....

- (a) only one *p-n* junction.      (b) three *p-n* junctions.  
(c) no *p-n* junctions.      **(d) two *p-n* junctions.**

**Answer**

The bipolar junction transistor consists of three regions: Emitter, Base and Collector. There is a junction between each two regions. So there are two junctions: one between emitter and base and the other between Base and collector.

7. A noninverting amplifier has  $R_F = 15 \text{ k}\Omega$  and  $R_i = 1.2 \text{ k}\Omega$ . How much is its voltage gain?

- (a) 12.5.      **(b) 12.5.**      (c) 13.5.      (d) 9.

**Answer**

$$A = R_F/R_i = 15/1.2 = 12.5$$